

IEEE EDTM Report Session

IEEE EDS, 3rd Electron Devices Technology and Manufacturing (EDTM) Conference 2019 (http://ewh.ieee.org/conf/edtm/2019/) was held on March 13-15, 2019.

The report session of EDTM-2019 will be held on July 12, 2019, as follows.

***** IEEE EDTM 2019 Report Session Info. ***** (Cooperated by IEEE EDS Japan Joint Chapter)

- Date: July 12, 2019 13:30-17:00
- Venue: Tokyo Institute of Technology, Suzukakedai Campus, G-Area Bldg. G2 Room#501 https://www.titech.ac.jp/maps/suzukakedai/
- Registration Fee: FREE

13:30-13:40	Summary of EDTM2019	Kazunari Ishimaru
		(Toshiba Memory Corp.)
13:40-14:10	Subcommittee on Si Device	es Masumi Saitoh
		(Toshiba Memory Corp.)
14:10-14:40	Subcommittee on Process	Makoto Miura
		(Hitachi High-Technologies Corp.)
14:40-15:10	Subcommittee on Modeling	Dondee Navarro
		(Hiroshima Univ.)
15:10-15:30	Coffee Break	
15:30-16:00	Subcommittee on Package	Yoichiro Kurita
		(Toshiba Corp.)
16:00-16:30	Subcommittee on Material	Hitoshi Wakabayashi
		(Tokyo Institute of Technology)
16:30-16:55	Summary of 3rd JST/CREST/2D Workshop	
		Hitoshi Wakabayashi
		(Tokyo Institute of Technology)
16:55-17:00	Introduction of EDTM2020	Kazunari Ishimaru
		(Toshiba Memory Corp.)
17:00	Closing	

IEEE EDTM 2020 will be held at Hotel Equatorial Penang, Malaysia, on March 16-18, 2020. Paper submission deadline will be September 27, 2019. The accepted paper will be published on IEEE Xplore Digital Library and J-EDS special issue. http://ewh.ieee.org/conf/edtm/2020/

■Contact

Kazunari Ishimaru (Toshiba Memory Corp.) kazu.ishimaru@toshiba.co.jp Yuichiro Mitani (Toshiba Memory Corp.) yuuichiro.mitani@toshiba.co.jp

IEEE EDS Japan Joint Chapter web site:

http://www.ieee-jp.org/japancouncil/chapter/ED-15/